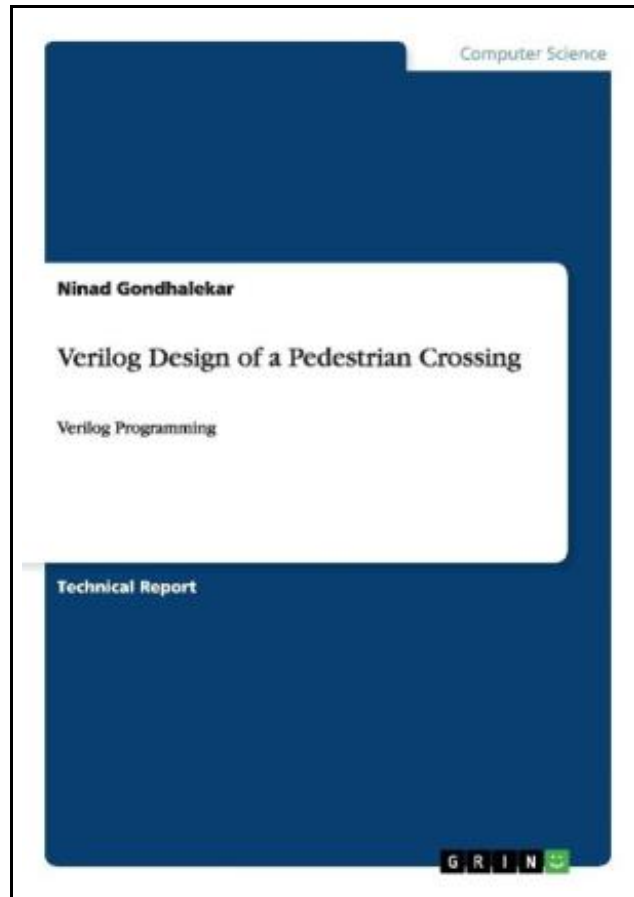# Verilog Design of a Pedestrian Crossing : Verilog Programming

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VERILOG DESIGN OF A PEDESTRIAN CROSSING : VERILOG PROGRAMMING

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GRIN Publishing Dez 2014, 2014. Taschenbuch. Condition: Neu. Neuware - Technical Report from the year 2013 in the subject Computer Science - Programming, grade: 80%, Northumbria University, course: BEng. Electrical and Electronic Engineering (Hons.), language: English, abstract: This laboratory results shows the Verilog hardware design language (HDL) and Finite State Machine to control a pedestrian crossing controller and its modification. In session-1, simple Verilog program is simulated and then tested on Coolrunner-II board. In this session the controller three outside world input which are Clock (CLK), Reset (RESET), Pedestrian (PED) and three output which are Red light (RED), Amber light (AMBER), Green light (GREEN).In session-2, some modifications are done on Verilog program used in session-1 and then again simulated and tested on Coolrunner-II board. In this session the controller 7 segment displays is used to see changes in pelstate, while running modified Verilog program on Coolrunner-II board and Carsensor outside world input has been introduced in modified Verilog program. In this modified Verilog program, number of pelstate has been increased. This laboratory session provides a good opportunity to learn Xilinx ISE Design Suit software and introduce to Coolrunner-II board. 24 pp. Englisch.

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